

Energy Efficient Autonomous Solar Water Pumping System

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Abstract -This paper proposes a new converter and inverter for autonomous operation of a solar water pumping system. This system uses a modified two inductor boost converter (TIBC) for supplying energy to a 3 ϕ induction motor through a three phase voltage source inverter (VSI). The TIBC has high voltage gain and low input current ripple. In this paper it is further improved with the use of non isolated recovery snubber to improve its efficiency. The 3 ϕ VSI uses space vector pulse width modulation technique (SVPWM) for producing pulses to control the MOSFET switches. By this technique the THD is reduced to 16.24% and the results are shown in MATLAB. As a result this system produces low torque ripple, low rise time and settling time for the induction motor operation and hence well suited for the operation of an autonomous solar water pump.

Index terms

Ac motor drives, dc-ac power conversion, dc-dc power conversion, solar power generation, space vector pulse width modulation

I. INTRODUCTION

Renewable energy sources exist over wide geographical areas, in contrast to other energy sources which are concentrated in a limited number of countries[1][2][3]. Rapid development of renewable energy and energy sufficiency is resulting in significant energy security, climate change mitigation and economic benefits. National renewable energy markets are projected to continue to grow strongly in the coming decade and beyond.

In countries such as interior parts of Brazil the unavailability of electric power supply to the people of that area, the need for pumping becomes difficult as by conventional means, so PV based water pumping systems are becoming more efficient in pumping water to the community[4]

Till now the converters used in Brazil are based on an intermediate storage system, which uses lead acid batteries and dc motors to drive the water pump[6]. Now the use of low voltage synchronous motors have been developed which gives high efficiency, but they are very expensive to be used in poor communities.

The batteries make the motor to operate at its rated voltage. This makes the coupling of solar panel and the motor. The batteries used have a short life span compared to the average useful life of PV panel. further, the lack of

battery replacement is another problem for such system to be used in isolated areas.

Many commercial systems use low voltage dc motors so that boost stage can be avoided between PV and motor. But dc motors have low efficiency and high maintenance cost as compared to the induction motor. Also low voltage dc motors are not easily available in markets. Because of this 3 ϕ induction motors are used in pumping system.

The design of a motor drive system supplied directly from solar demands constant power to be supplied which creates a need for a converter with the following features. High efficiency due to low energy available, low cost, robustness and autonomous operation.

This paper proposes a new dc/dc converter and a three phase VSI with space vector pulse width modulation control for the MOSFET switches. The paper is organised as follows. In section II the dc/dc converter is described. In section III the 3 ϕ VSI is described and the section IV describes the simulation results obtained from MATLAB.

II. TIB CONVERTER

In a number of high power applications, the performance of boost converter is improved by implementing the boost converter with multiple switches and multiple boost inductors as shown in fig.1. This topology is usually employed in high power applications with a high input voltage or in applications where the conversion efficiency of a single switch boost topology is significantly degraded by the reverse recovery losses of the boost rectifier.

A two switch two inductor boost converter achieves output voltage regulation in a wide input and load current range using a constant frequency by employing an auxiliary transformer to couple current path of the two boost inductors so that both the inductors carry the same current. By forcing the current through the boost inductors to be the same the energy in both the inductors is forced to change simultaneously that is both the inductors increase the energy when both switches are turned on and simultaneously decrease energy when either of the converter two switches is turned off.

As a result the stored and transferred energy of both the inductors can be controlled in a wide input voltage and load range using a constant frequency control by

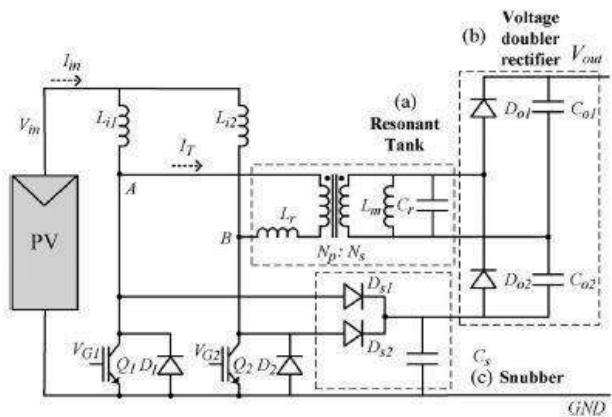


Fig.1.modified TIBC topology

controlling the time duration that the two switches are simultaneously on.

The TIBC must have a minimum operation load to maintain an established output voltage. Below a certain load level, the energy transferred to the output capacitor is not completely transferred to the load and causes an increase in the output voltage. This happens because of the inductors are charged even if there is no output current.

As a result, this converter has a drawback when used in motor drive system. As a solution for the drawback, a non dissipative regenerative snubber circuit is presented in the modified TIBC. The regenerative snubber is formed by two diodes and the capacitor connecting the input side directly to the output side of the converter[7] This makes it a non isolated converter which has no desirable effect in the PV motor driver applications

To solve the minimum load conditions, the use of a hysteresis controller for the dc output voltage of the first stage is proposed[8]. When operating below the minimum required load an uncontrolled increase of output voltage will be seen and once this voltage reaches the upper threshold, both primary switches are turned off. This should be impossible in all the previously reported implication of TIBC. However with an added snubber even with both switches turned off, there is still a path for the input inductors current. This energy is directly transferred to the snubber capacitor Cr.

Once the multi resonant tank is introduced, two different resonant processes occur 1) When both switches are closed, the leakage inductance Lr participates along with capacitance Cr in the resonance at the primary current switching and current polarity inversion and 2) during the conduction time interval when at least one of the switches is open, Lr is associated in series with Lm, or Lij not participating on the transformer's secondary current resonance, formed only by Lm and Cr.

At time t1, the rectifying diode D01 is already conducting, and the voltage on resonant capacitor Cr is clamped at Vout/2. At this instant, the switch Q1 is activated by Vgq1. At the time t2, the voltage Vout starts to increase,

Q2 is completely blocked, and the snubber diode Ds1 begins to conduct, transferring energy directly to the snubber capacitor Cs. Between t2 and t3, Lr and Lm continue to resonate, decreasing the voltage on the doubler rectifier's input and on Vcr. At instant t4, the voltage across Cr reaches -Vout/2, and the rectifying diode D02 starts to conduct, clamping Vcr in -Vout/2.

From t4 to t5, the capacitor Cr is charged and the current of D02 starts to decrease. At the instant t5, Q2 is turned on initiating the resonant process on Cr. As Cr is activated, D01 is forced to stop conduction. At the instant t6, the current in D02 reaches zero, and D02 stops conducting re-initiating the resonance between Lr and Lm [9]. From this moment until the end of the switching period, the process repeats symmetrically as explained for the other input switch.

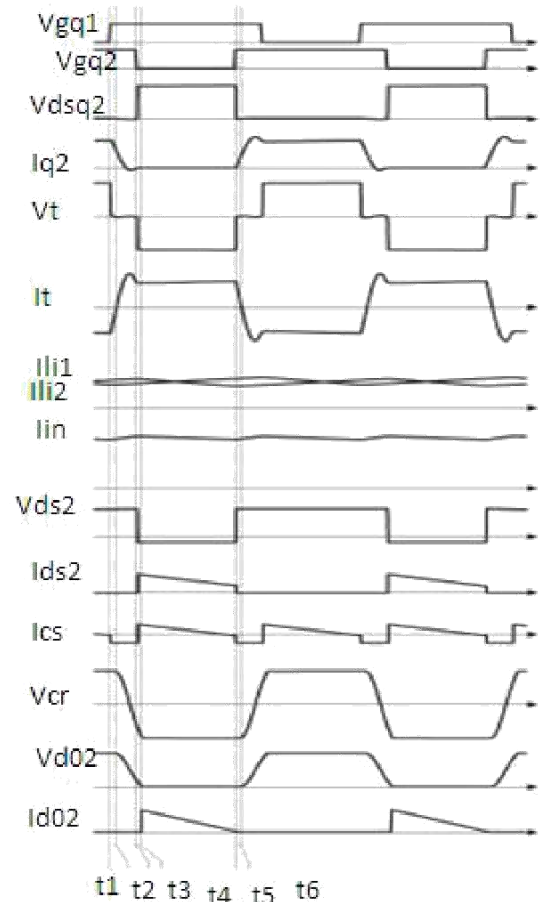


Fig.2. Waveforms for TIBC Operation

III. THREE PHASE VSI

The output voltage from modified two inductor boost converter is given to a 3φ voltage source inverter (VSI). The 3φ VSI consists of 6 MOSFET switches. The gate signals to the MOSFET are given by the Space Vector Pulse Width Modulation technique (SVPWM)[8]. The use of this PWM strategy is to improve the output voltage level as compared to sinusoidal pulse width modulation strategy with 1/6 optimal third harmonic voltage injection.

Vector	A ⁺	B ⁺	C ⁺	A ⁻	B ⁻	C ⁻	V _{AB}	V _{BC}	V _{CA}	
V ₀ = {000}	OFF	OFF	OFF	ON	ON	ON	0	0	0	zero vector
V ₁ = {100}	ON	OFF	OFF	OFF	ON	ON	+V _{dc}	0	-V _{dc}	active vector
V ₂ = {110}	ON	ON	OFF	OFF	OFF	ON	0	+V _{dc}	-V _{dc}	active vector
V ₃ = {010}	OFF	ON	OFF	ON	OFF	ON	-V _{dc}	+V _{dc}	0	active vector
V ₄ = {011}	OFF	ON	ON	ON	OFF	OFF	-V _{dc}	0	+V _{dc}	active vector
V ₅ = {001}	OFF	OFF	ON	ON	ON	OFF	0	-V _{dc}	+V _{dc}	active vector
V ₆ = {101}	ON	OFF	ON	OFF	ON	OFF	+V _{dc}	-V _{dc}	0	active vector
V ₇ = {111}	ON	ON	ON	OFF	OFF	OFF	0	0	0	zero vector

Table 1. Switching Vector Table

SVPWM is an algorithm for the control of pulse width Modulation. It is used for the creation of alternating current waveforms most commonly used to drive three phase AC powered motor at varying speeds[9][10].

Now looking down the columns for active switching vectors $V_1 - V_6$, the output voltages vary as a pulsed sinusoid, with each leg offset by 120 degrees of base angle[11][12]. The desired three phase voltages at the output of the inverter could be represented by an equivalent vector V rotating in counter clockwise direction[13]. The magnitude of this vector is related to the magnitude of the output voltage and the time this vector takes to complete one revolution is the same as the fundamental time period of the output

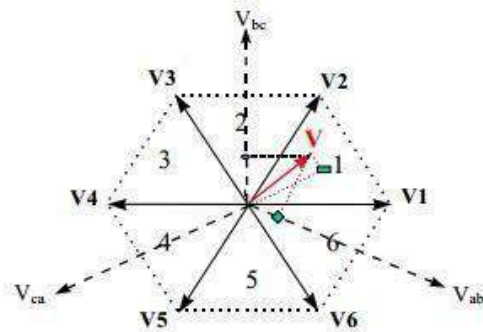


Fig.3. Output voltage vector in α, β plane

Let us consider the situation when the desired line to line output voltage vector V is in sector 1. This vector could be synthesized by the pulse with modulation of two adjacent state space vector's (SSV) V_1 and V_2 , the duty cycle of each being d_1 and d_2 respectively and the zero vector of duty cycle d_3 .

$$d_1 V_1 + d_2 V_2 = V$$

$$= mV_g e^{j\theta}$$

$$d_1 + d_2 + d_3 = 1$$

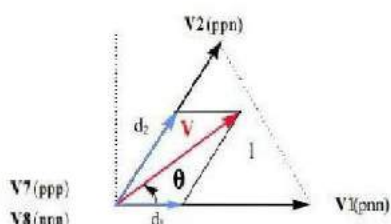


Fig.4. Synthesis of the Output Voltage Vector in Sector 1

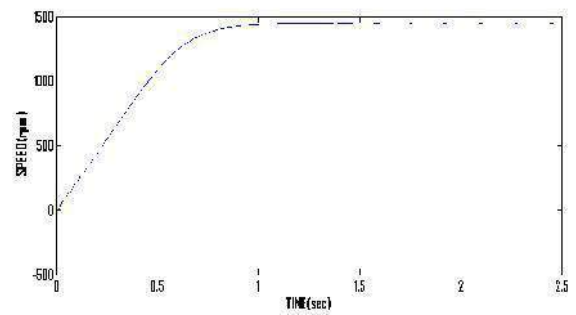


Fig.5. Speed Curve

Where $0 \leq m \leq 0.866$ is the modulation index[14]. This would correspond to a maximum line to line voltage of 1.0 vg which is 15% more than conventional sinusoidal plus 1/6th optimal third harmonic injection PWM.

If this algorithm uses non adjacent SSV's then it will produce higher THD and switching losses. This output waveform for all the six sectors are shown in Fig 3.5. These six sectors together form a cycle of period $T_s = 1/f_s$.

The THD of a periodic voltage which can be represented by the Fourier series

$$V = \sum_{n=1}^{\infty} V_n \sin(n\omega t)$$

is defined as

$$THD = \sqrt{\sum_{n=2}^{\infty} V_n^2 / V_1}$$

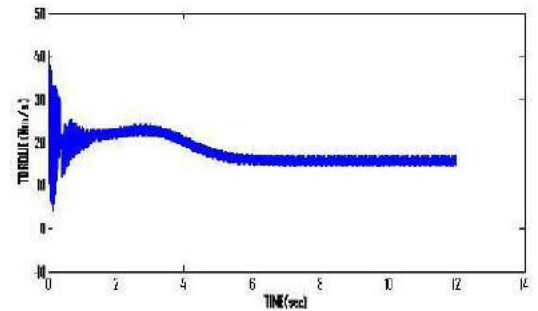


Fig.6. torque curve

IV. SIMULATION RESULTS

The space vector simulation circuit is generated using a rotating vector in the $\alpha\beta$ plane. The voltage in the $\alpha\beta$ plane is compared with a reference voltage and the PWM pulses are generated in the rotating reference plane[15].

The simulink circuit for space vector pulse width modulation is shown in fig.8. The speed and torque curves for space vector pulse width modulation is shown in fig.5,6 respectively.

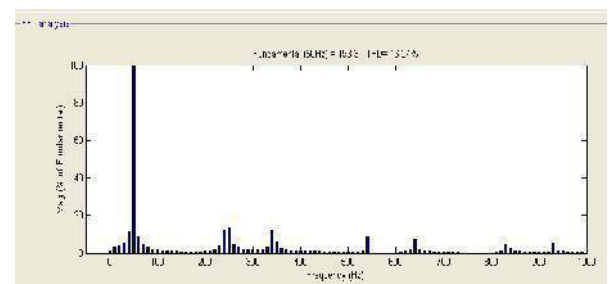


Fig.7.FFTanalysis

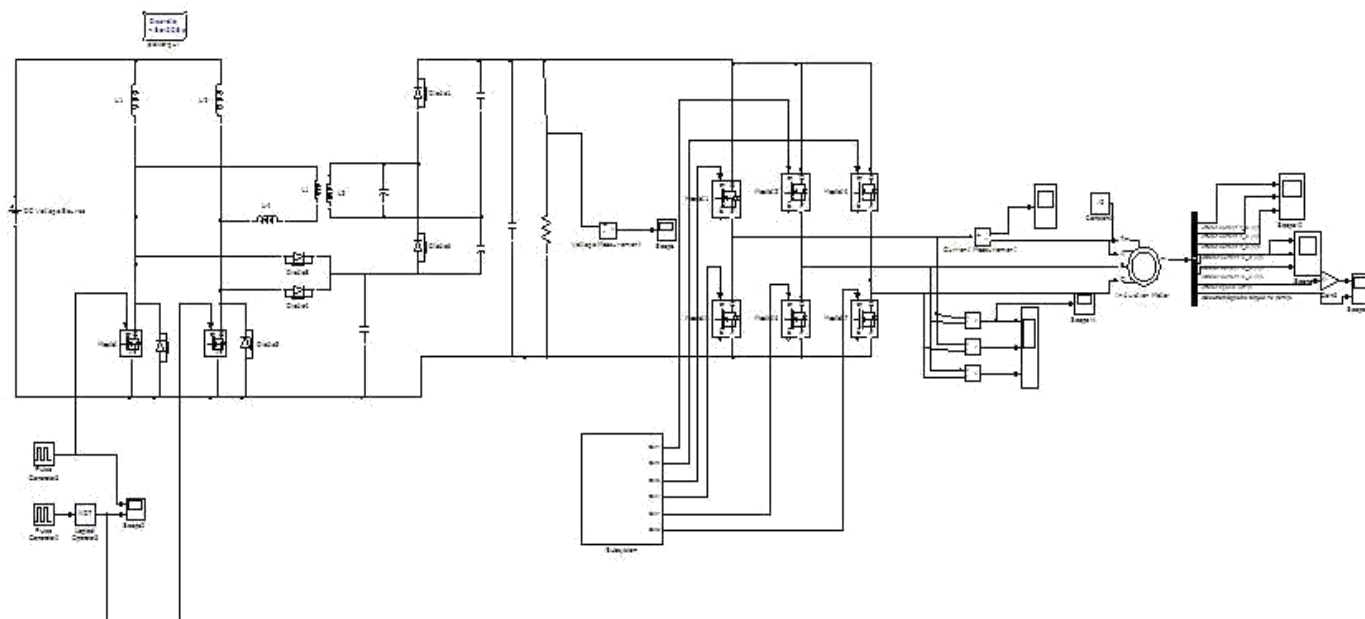


Fig.8.SVPWM simulation

V. CONCLUSION

From the adopted control method (SVPWM) and FFT analysis(fig.7) carried out the total harmonic distortion (THD) is reduced to 20% as compared to sinusoidal pulse width modulation with 1/6th optimal third harmonic voltage injection method as is shown in table below. The induction motor performance is also improved. This work can be carried out with the soft computing techniques for giving pulses to the switches for better results.

	SPWM WITH 3 RD HARMONIC INJECTION	SPACE VECTOR PWM
THD	36.4%	16.4%
RISE TIME	4.2 sec	1.2 sec
SETTLING TIME	5.9 sec	1.3 sec
TORQUE RIPPLE	15%	11.76%

Table.2.comparison table

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